

## SPECIFICATION

**CUSTOMER :** \_\_\_\_\_

**MODULE NO.:**           **WG240128B-FMI-VZ#**          

<p style="text-align: center;"><b>APPROVED BY:</b></p> <p style="text-align: center;">( FOR CUSTOMER USE ONLY )</p>	<p><b>PCB VERSION:</b>                      <b>DATA:</b></p>
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
<b>ISSUED DATE:</b>			

MODLE NO□

**RECORDS OF REVISION**

**DOC. FIRST ISSUE**

VERSION	DATE	REVISED PAGE NO.	<b>SUMMARY</b>
0	2007/12/21		First issue

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# 1. Module Classification Information

W G    2 4 0 1 2 8    B F M I    V Z #  
 ① ②                      ③                      ④    ⑤ ⑥ ⑦                      ⑧

- ① Brand  WINSTAR DISPLAY CORPORATION
- ② Display Type  H  Character Type, G  Graphic Type
- ③ Display Font  240 \* 128
- ④ Model serials no.
- ⑤ Backlight Type 
  - N  Without backlight
  - T  LED, White
  - B  EL, Blue green
  - A  LED, Amber
  - D  EL, Green
  - R  LED, Red
  - W  EL, White
  - O  LED, Orange
  - F  CCFL, White
  - G  LED, Green
  - Y  LED, Yellow Green
- ⑥ LCD Mode 
  - B  TN Positive, Gray
  - T  FSTN Negative
  - N  TN Negative,
  - G  STN Positive, Gray
  - Y  STN Positive, Yellow Green
  - M  STN Negative, Blue
  - F  FSTN Positive
- ⑦ LCD Polarizer Type/  
Temperature range/  
View direction
  - A  Reflective, N.T, 6:00
  - H  Transflective, W.T,6:00
  - D  Reflective, N.T, 12:00
  - K  Transflective, W.T,12:00
  - G  Reflective, W. T, 6:00
  - C  Transmissive, N.T,6:00
  - J  Reflective, W. T, 12:00
  - F  Transmissive, N.T,12:00
  - B  Transflective, N.T,6:00
  - I  Transmissive, W. T, 6:00
  - E  Transflective, N.T.12:00
  - L  Transmissive, W.T,12:00
- ⑧ Special Code
  - V : Build in Negative voltage ;    Z:ICNT7086
  - #:Fit in with the ROHS Directions and regulations

## 2. Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) Winstar have the right to change the passive components
- (9) Winstar have the right to change the PCB Rev.

## 3. General Specification

ITEM	STANDARD VALUE	UNIT
Number of Dots:	240 ×128	
Module dimension:	152.0×104.0×14.3(MAX)mm	mm
View area:	114.0×64.0mm	mm
Active area:	107.98×57.58mm	mm
Character size:	(L)0.43×(W)0.43 mm	mm
Character pitch:	(L)0.45×(W)0.45mm	mm
LCD type:	STN Negative, Blue Transmissive	
Duty:	1/128	
View direction:	6 o'clock	
Backlight:	CCFL White	

## 4. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Operating Temperature	$T_{OP}$	-20	□	+70	□
Storage Temperature	$T_{ST}$	-30	□	+80	□
Input Voltage	$V_I$	$V_{SS}$	□	$V_{DD}$	V
Supply Voltage For Logic	$V_{DD}-V_{SS}$	-0.3	□	+7	V
Supply Voltage For LCD	$V_{DD}-V_0$	0	□	21	V

## 5. Electrical Characteristics

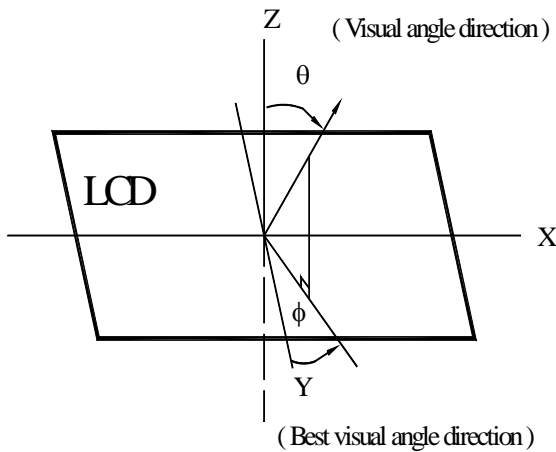
ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage For Logic	$V_{DD}-V_{SS}$	□	4.75	5.0	5.25	V
Supply Voltage For LCD	$V_{DD}-V_0$	$T_a=-20$ □	□	□	21.7	V
		$T_a=25$ □	□	19.5	□	V
		$T_a=70$ □	17.6	□	□	V
Input High Vol	$V_{IH}$	□	$V_{DD}-2.2$	□	$V_{DD}$	V
Input Low Vol	$V_{IL}$	□	0	□	0.8	V
Output High Vol	$V_{OH}$	□	$V_{DD}-0.3$	□	$V_{DD}$	V
Output Low Vol.	$V_{OL}$	□	0	□	0.3	V
Supply Current	$I_{DD}$	$V_{DD}=5V$	□	45	□	mA

## 6. Optical Characteristics

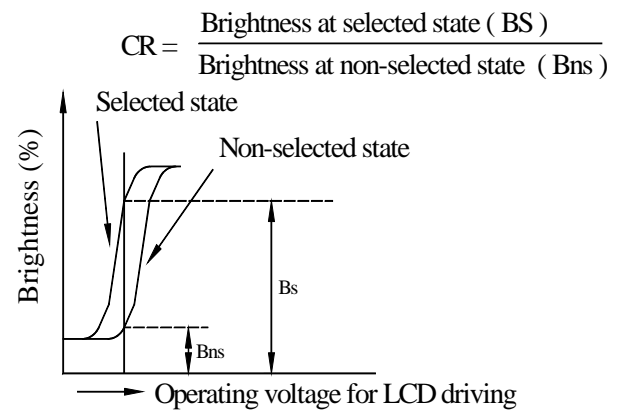
ITEM	SYMBAL	CONDITION	MIN.	TYP.	MAX.	UNIT
View Angle	(V)□	CR□2	20		40	deg
	(H)□	CR□2	-30		30	deg
Contrast Ratio	CR	□		3		□
Response Time	T rise	□		200	300	ms
	T fall	□		200	300	ms

### 6.1 Definitions

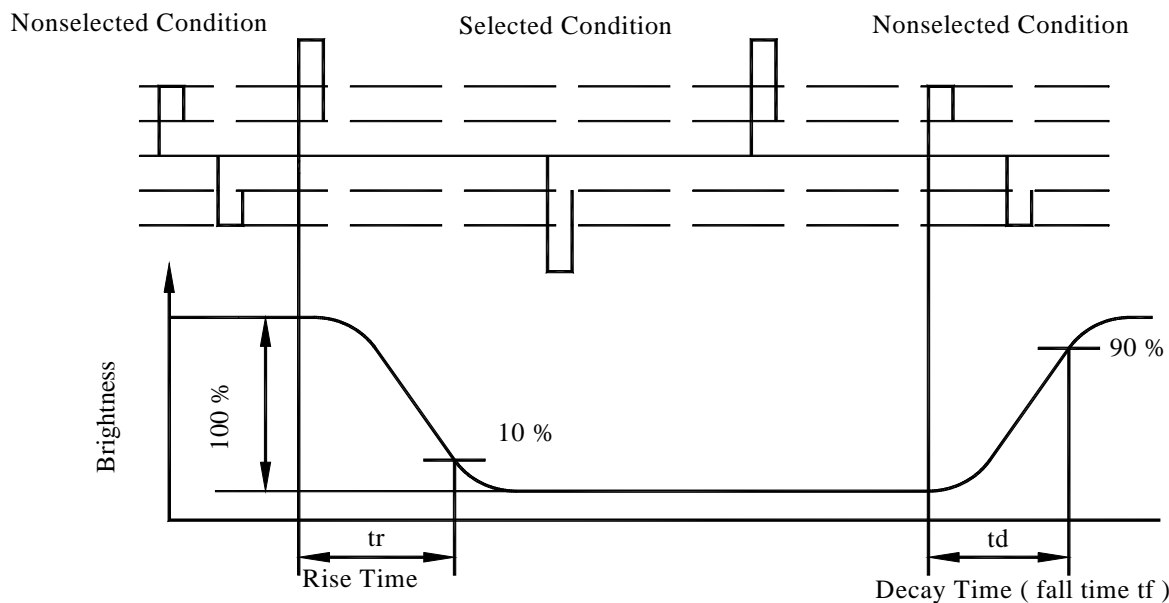
#### ■ View Angles



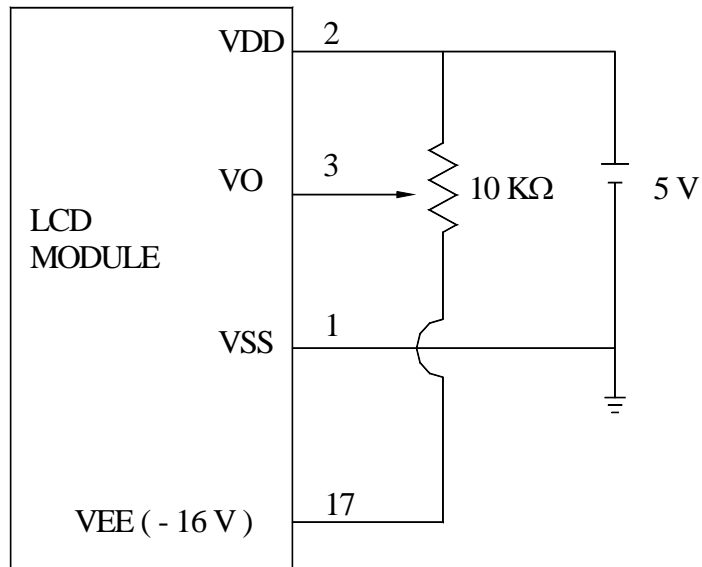
#### ■ Contrast Ratio



#### □ Response Time



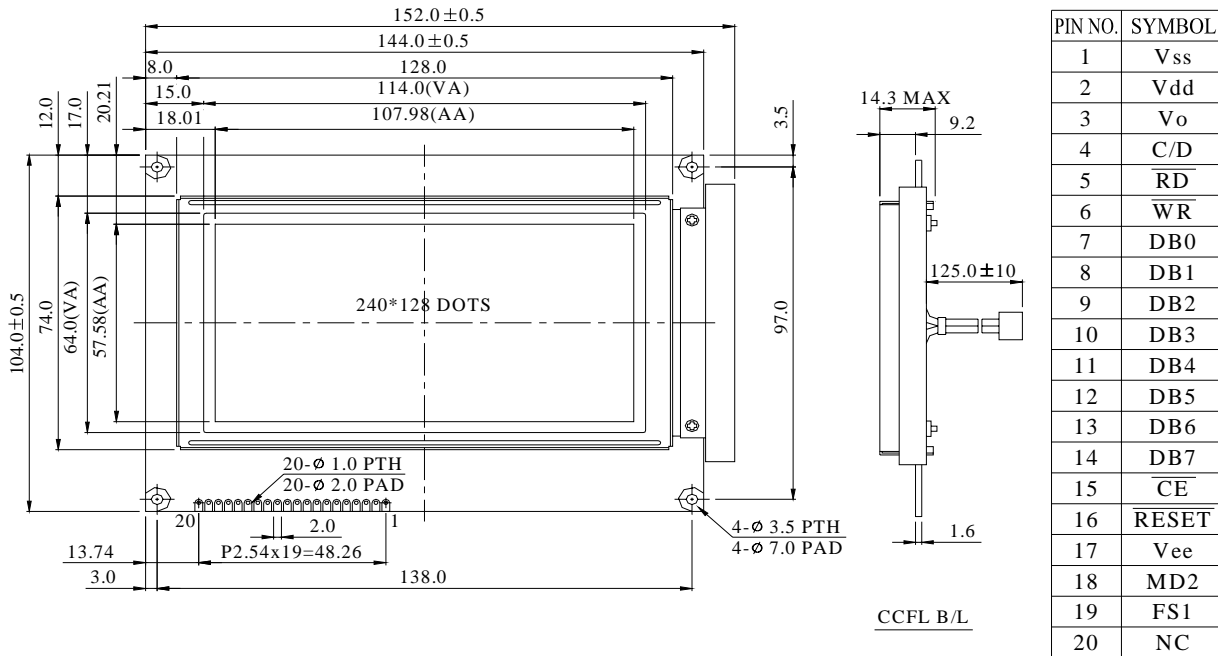
## 7. Power Supply for LCD Module and Contrast Adjust



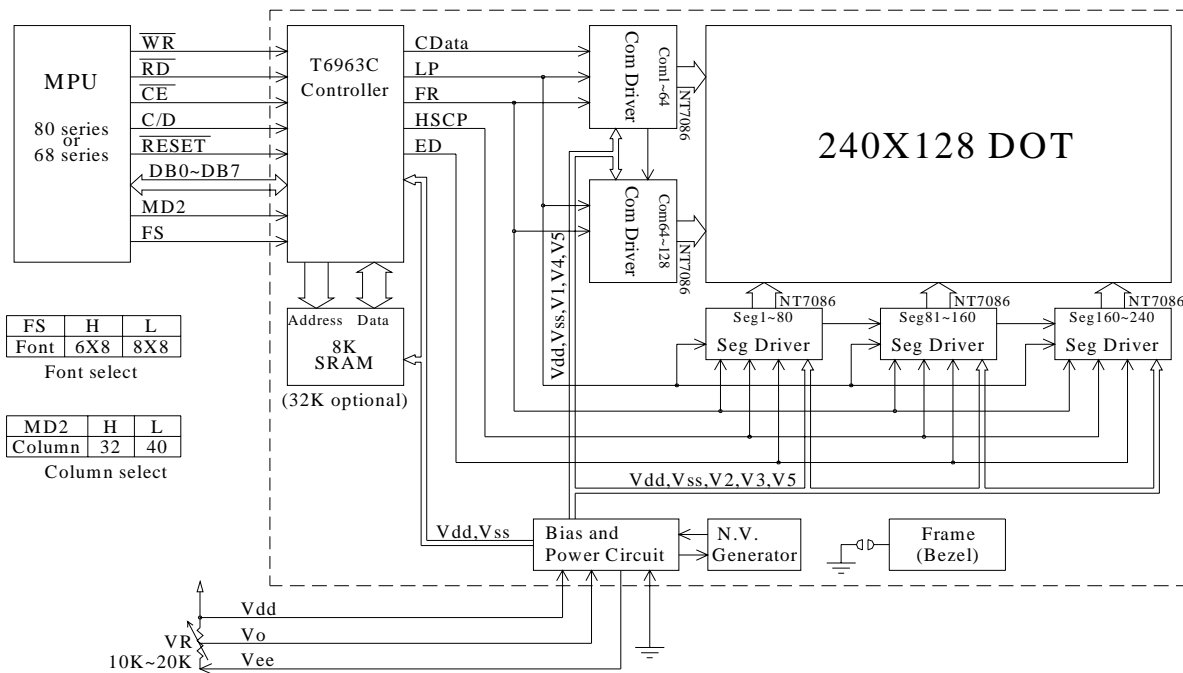
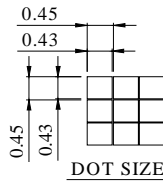
VDD-V0:LCD Operating Voltage



# 8. Counter Drawing & Block Diagram



The non-specified tolerance of dimension is ±0.3mm.



External contrast adjustment.

## 9. Interface Pin Function

Pin No.	Symbol	Level	Description
1	Vss		GND
2	Vdd		Power supply ( +5 V )
3	Vo		Power supply for LCD driver
4	C/D	H / L	WR=L , C/D=H : Command Write    C/D=L: Data write RD=L , C/D=H : Status Read    C/D=L: Data read
5	RD	L	Data read. Read data from T6963C when RD = L
6	WR	L	Data write. Write data into T6963C when WR = L
7	DB0	H / L	Data bus line
8	DB1	H / L	Data bus line
9	DB2	H / L	Data bus line
10	DB3	H / L	Data bus line
11	DB4	H / L	Data bus line
12	DB5	H / L	Data bus line
13	DB6	H / L	Data bus line
14	DB7	H / L	Data bus line
15	CE	L	L : Chip enable
16	RESET	H / L	H : Normal ; L : Initialize T6963C
17	Vee		Negative Voltage output
18	MD2	H / L	H: 32 columns ; L: 40 columns
19	FS1	H / L	Pins for selection of font ; H : 6 * 8 , L : 8 * 8
20	NC		No connection

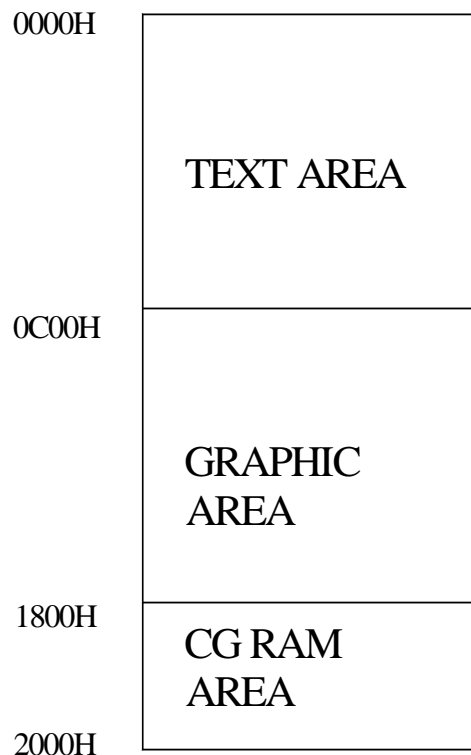
# 10. Display control instruction

The LCD Module has built in a T6963C LSI controller, It has an 8-bit parallel data bus and control lines for writing or reading through an MPU interface, it has a 128-word character generator ROM ( refer to Table 1. ), which can control an external display RAM of up to 8K bytes. Allocation of text, graphics and external character generator RAM can be made easily and the display window can be moved freely within the allocated memory range.

## •RAM Interface

The external RAM is used to store display data( text, graphic and external CG data ). It can be freely allocated to the memory area( 8 Kbyte max ).

Recommend



□ Flowchart of communications with MPU

(1) Status Read

A status check must be performed before data is read or written.

Status check

The Status of T6963C can be read from the data lines.

$\overline{RD}$	L
$\overline{WR}$	H
$\overline{CE}$	L
C/D	H
Do to D7	H

The T6963C status word format is as follows:

MSB				LSB			
STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0

STA0	Check command execution capability	0:Disable 1:Enable
STA1	Check data read/write Capability	0:Disable 1:Enable
STA2	Check Auto mode data read capability	0:Disable 1:Enable
STA3	Check Auto mode data write capability	0:Disable 1:Enable
STA4	Not used	
STA5	Check controller operation capability	0:Disable 1:Enable
STA6	Error flag. Used for Screen Peek and Screen copy commands.	0:No error 1:Error
STA7	Check the blink condition	0:Disable off 1:Normal display

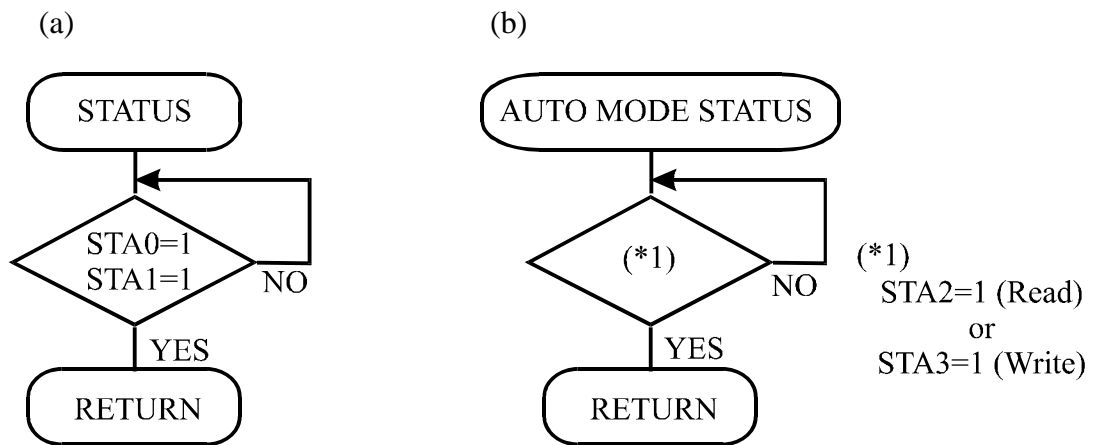
(Note 1) It is necessary to check STA0 and STA1 at the same time.

There is a possibility of erroneous operation due to a hardware interrupt.

(Note 2) For most modes STA0/STA1 are used as a status check.

(Note 3) STA2 and STA3 are valid in Auto mode; STA0 and STA1 are invalid.

## Status Checking flow



(Note 4) When using the MSB=0 command, a Status Read must be performed.

If a status check is not carried out, the T6963C cannot operate normally, even after a delay time.

The hardware interrupt occurs during the address calculation period (at the end of each line).

If a MSB=0 command is sent to the T6963C during this period, the T6963C enters Wait status.

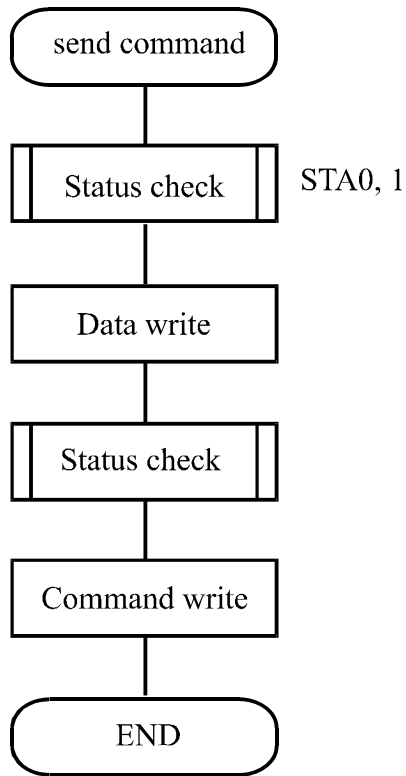
If a status check is not carried out in this state before the next command is sent, there is the possibility that the command or data data will not be received.

## (2)Setting date

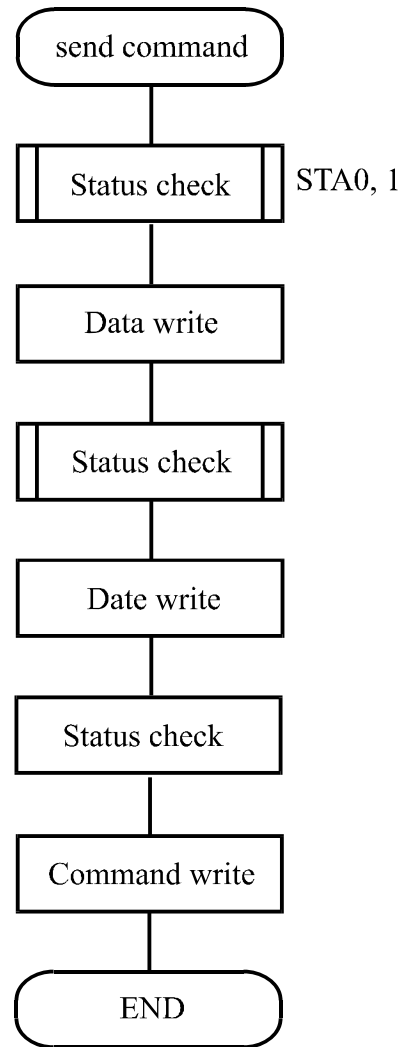
When using the T6963C, first set the data, then set the command.

### Procedure for sending a command

(a)The case of 1 date



(b)The case of 2 data



(Note) When sending more than two data, the last datum (or last two data)is valid.

□ COMMAND DEFINITIONS

COMMAND	CODE	D1	D2	FUNCTION
REGISTERS SETTING	00100001	X address	Y address	Set Cursor Pointer
	00100010	Date	00H	Set Offset Register
	00100100	Low address	High address	Set Address Pointer
SET CONTROL WORD	01000000	Low address	High address	Set Text Home Address
	01000001	Columns	00H	Set Text Area
	01000010	Low address	High address	Set Graphic Home Address
	01000011	Columns	00H	Set Graphic Area
MODE SET	1000×000	<input type="checkbox"/>	<input type="checkbox"/>	OR mode
	1000×001	<input type="checkbox"/>	<input type="checkbox"/>	EXOR mode
	1000×011	<input type="checkbox"/>	<input type="checkbox"/>	AND mode
	1000×100	<input type="checkbox"/>	<input type="checkbox"/>	Text Attribute mode
	10000×××	<input type="checkbox"/>	<input type="checkbox"/>	Internal CG ROM mode
	10001×××	<input type="checkbox"/>	<input type="checkbox"/>	External CG RAM mode
DISPLAY MODE	10010000	<input type="checkbox"/>	<input type="checkbox"/>	Display off
	1001××10	<input type="checkbox"/>	<input type="checkbox"/>	Cursor on, blink off
	10001××11	<input type="checkbox"/>	<input type="checkbox"/>	Cursor on, blink on
	100101××	<input type="checkbox"/>	<input type="checkbox"/>	Text on, graphic off
	100110××	<input type="checkbox"/>	<input type="checkbox"/>	Text off, graphic on
	100111××	<input type="checkbox"/>	<input type="checkbox"/>	Text on, graphic on
CURSOR PATTERN SELECT	10100000			1-line cursor
	10100001	<input type="checkbox"/>	<input type="checkbox"/>	2-line cursor
	10100010	<input type="checkbox"/>	<input type="checkbox"/>	3-line cursor
	10100011	<input type="checkbox"/>	<input type="checkbox"/>	4-line cursor
	10100100	<input type="checkbox"/>	<input type="checkbox"/>	5-line cursor
	10100101	<input type="checkbox"/>	<input type="checkbox"/>	6-line cursor
	10100110	<input type="checkbox"/>	<input type="checkbox"/>	7-line cursor
	10100111	<input type="checkbox"/>	<input type="checkbox"/>	8-line cursor
DATA AUTO READ/WRITE	10110000	<input type="checkbox"/>	<input type="checkbox"/>	Set Data Auto Write
	10110001	<input type="checkbox"/>	<input type="checkbox"/>	Set Data Auto Read
	10110010	<input type="checkbox"/>	<input type="checkbox"/>	Auto Reset
DATA READ/WRITE	11000000	Data	<input type="checkbox"/>	Data Write and Increment ADP
	11000001	<input type="checkbox"/>	<input type="checkbox"/>	Data Read and Increment ADP
	11000010	Data	<input type="checkbox"/>	Data Write and Decrement ADP
	11000011	<input type="checkbox"/>	<input type="checkbox"/>	Data Read and Decrement ADP
	11000100	Data	<input type="checkbox"/>	Data Write and Nonvariable ADP
	11000101	<input type="checkbox"/>	<input type="checkbox"/>	Data Read and Nonvariable ADP
SCREEN PEEK	11100000	<input type="checkbox"/>	<input type="checkbox"/>	Screen Peek

X : invalid

COMMAND	CODE	D1	D2	FUNCTION
---------	------	----	----	----------

SCREEN COPY	11101000			Screen Copy
BIT SET/RESET	11110×××	<input type="checkbox"/>	<input type="checkbox"/>	Bit Reset
	11111×××	<input type="checkbox"/>	<input type="checkbox"/>	Bit Set
	1111×000	<input type="checkbox"/>	<input type="checkbox"/>	Bit 0 (LSB)
	11111×001	<input type="checkbox"/>	<input type="checkbox"/>	Bit 1
	11111×010	<input type="checkbox"/>	<input type="checkbox"/>	Bit 2
	11111×011	<input type="checkbox"/>	<input type="checkbox"/>	Bit 3
	11111×100	<input type="checkbox"/>	<input type="checkbox"/>	Bit 4
	11111×101	<input type="checkbox"/>	<input type="checkbox"/>	Bit 5
	11111×110	<input type="checkbox"/>	<input type="checkbox"/>	Bit 6
	11111×111	<input type="checkbox"/>	<input type="checkbox"/>	Bit 7 (MSB)

X : invalid

Setting registers

CODE	HEX.	FUNCTION	D1	D2
00100001	21H	SET CURSOR POINTER	X ADRS	Y ADRS
00100010	22H	SET OFFSET REGISTER	DATA	00H
00100100	24H	SET ADDRESS POINTER	LOW ADRS	HIGH ADRS

(1) Set Cursor Pointer

The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only be moved by this command. Data read/write from the MPU never changes the cursor pointer. X ADRS and Y ADRS are specified as follows.

X ADRS      00H to 4FH (lower 7 bits are valid)

Y ADRS      00H to 1FH (lower 5 bits are valid)

Single-Scan

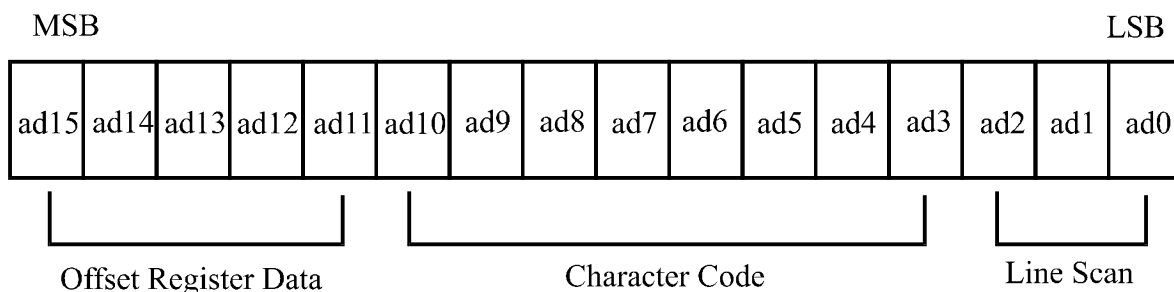
X ADRS 00 to 4FH

Y ADRS 00H to 0FH
-------------------

(2) Set Offset Register

The offset register is used to determine the external character generator RAM area.

The T6963C has a 16-bit address bus as follows.





T6963C assign External character generator, when character code set 80H TO FFH in using internal character generator. Character code 00H to 80H assign External character generator, when External generator mode.

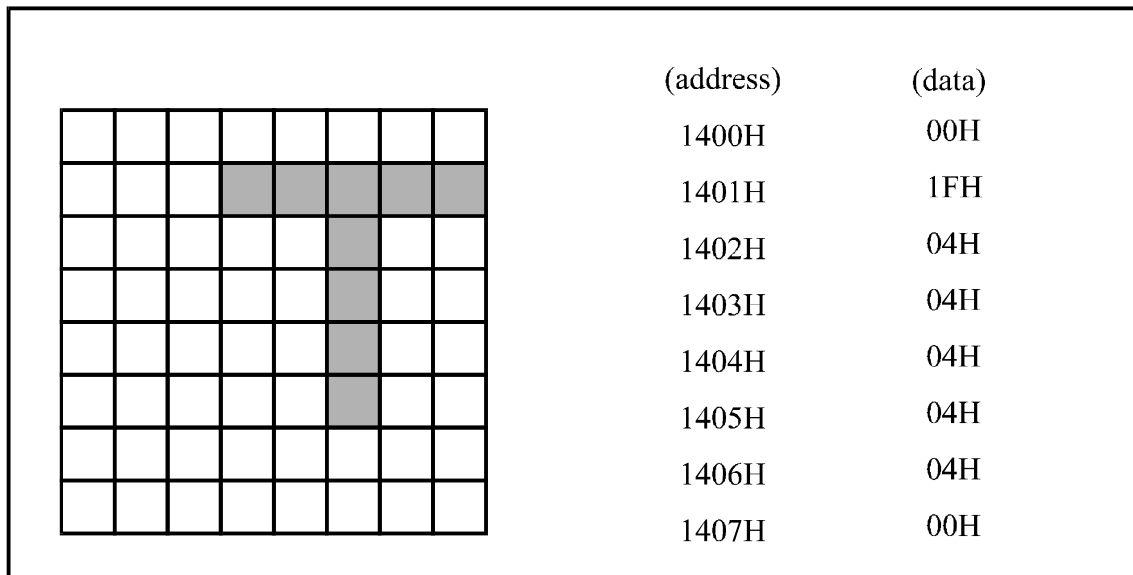
The senior five bits define the start address in external memory of the CG RAM area. The next eight bits represent the character code of the character. In internal CG ROM, character codes 00H to 7FH represent the predefined “internal” CG ROM characters, and codes 80H to FFH represent the user’s own “external” characters. In external CG ROM mode, all 256 codes from 00H to FFH can be used to represent the user’s own characters. The three least significant bits indicate one of the eight rows of eight dots that define the character’s shape.

The relationship between display RAM address and offset register

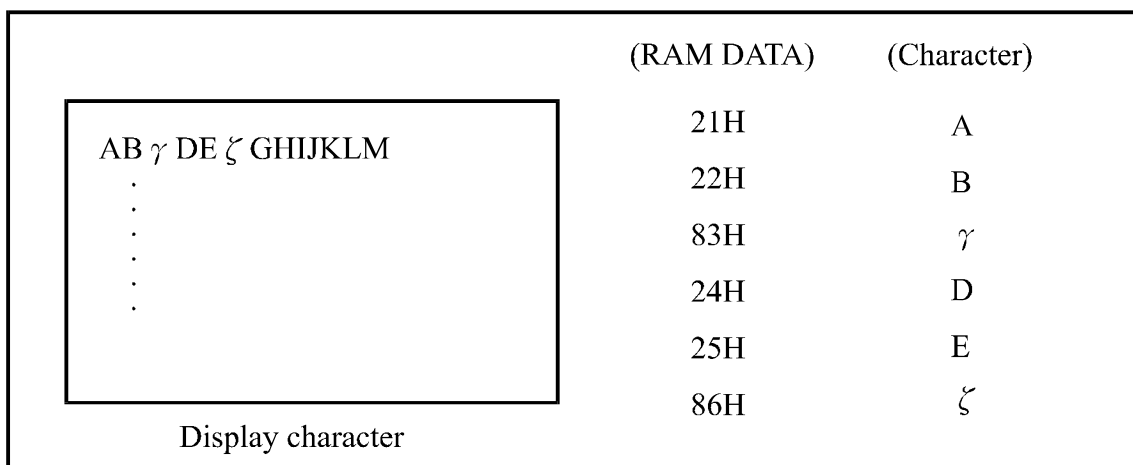
Offset register data	CG RAM hex. address (start to end)
00000	0000 to 07 FFH
00001	0800 to 0FFFH
00010	1000 to 17FFH
11100	E000 to E7FFH
11101	E800 to EFFFH
11110	F000 to F7FFH
11111	F800 to FFFFH

(Example 1)

Offset register	02H
Character code	80H
Character generator RAM start address	0001 0100 0000 0000
	1 4 0 0 H



(Example 2) The relationship between display RAM data and display characters

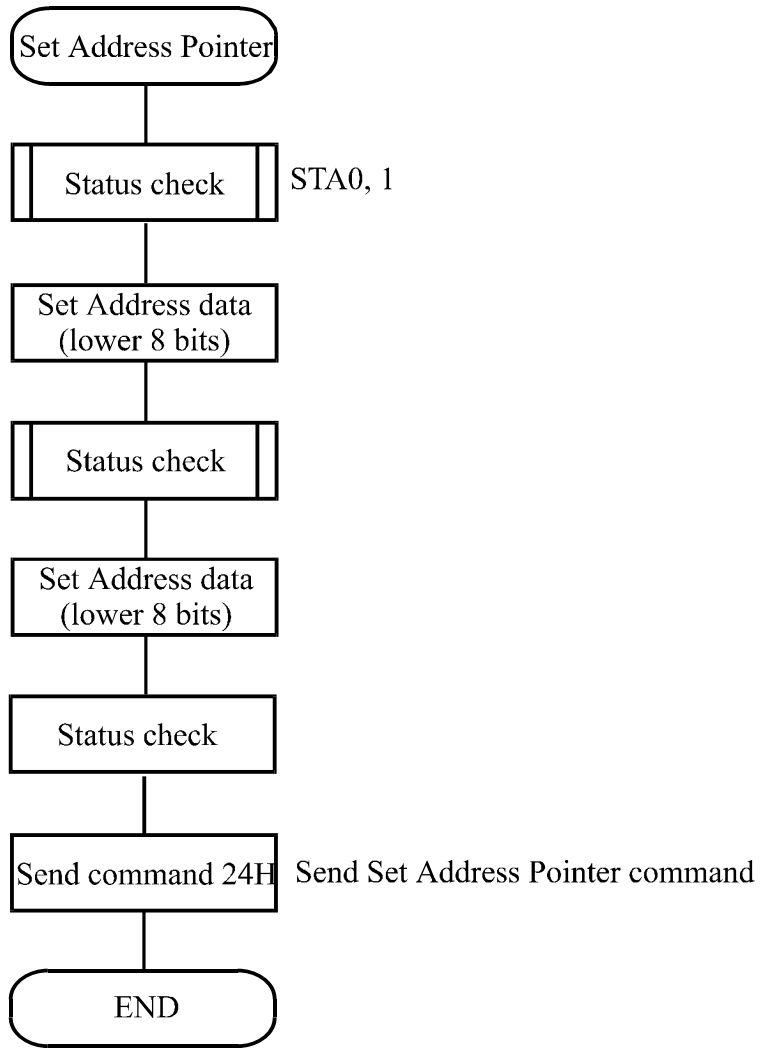


□ and □ are displayed by character generator RAM.

### (3) Set Address Pointer

The Set Address Pointer command is used to indicate the start address for writing to (or reading from) external RAM.

#### The Flowchart for Set Address Pointer command



Set Control Word

CODE	HEX.	FUNCTION	D1	D2
01000000	40H	Set Text Home Address	Low address	High address
01000001	41H	Set Text Area	Columns	00H
01000010	42H	Set Graphic Home Address	Low address	High address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1) Set Text Home Address

The starting address in the external display RAM for text display is defined by this command.

The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH		TH+CL
TH+TA		TH+TA+CL
(TH+TA)+TA		TH+2TA+CL
(TH+2TA)+TA		TH+3TA+CL
TH+(n-1)TA		TH+(n-1)TA+CL

TH:Text home address

TA:Text area number (columns)

CL:Columns are fixed by hardware (pin-programmable).

(Example)

Text home address

0000H

Text area

0020H

32 Columns

4 Lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	002FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH

## (2)Set Graphic Home Address

The starting address of the external display RAM used for graphic display is defined by this command.

The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH		GH+GL
GH+GA		GH+GA+CL
(GH+GA)+GA		GH+2GA+CL
(GH+2GA)+GA		GH+3GA+CL
GH+(n-1)GA		GH+(n-1)GA+CL

GH:Graphic home address

GA:Graphic area number (columns)

CL:Columns are fixed by hardware (pin-programmable).

(Example)

Graphic home address

0000H

Graphic area

0020H

32 Columns

2 Lines





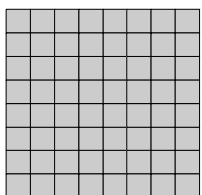
Mode set

CODE	FUNCTION	OPERAND
1000×000	OR Mode	<input type="checkbox"/>
1000×001	EXOR Mode	<input type="checkbox"/>
1000×011	AND Mode	<input type="checkbox"/>
1000×100	TEXT ATTRIBUTE Mode	<input type="checkbox"/>
10000×××	Internal Character Generator Mode	<input type="checkbox"/>
10001×××	External Character Generator Mode	<input type="checkbox"/>

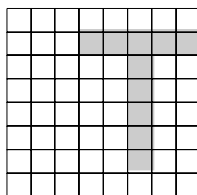
X:invalid

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In Internal Character Generator mode, character codes 00H to 7FH are assigned to the built-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

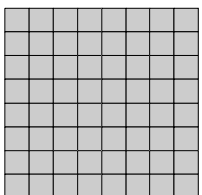
(Example)



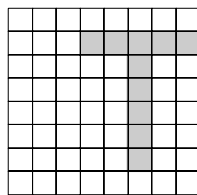
GRAPHIC



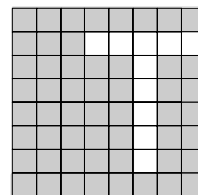
TEXT



“OR”



“AND”



“TXOR”

(Note)Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.

### Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode; graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.



The attribute data for each character in the text area is written to the same address in the graphic area. The Attribute function is defined as follows.

Attribute RAM 1byte



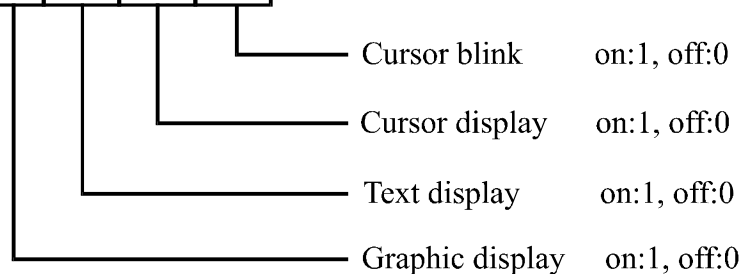
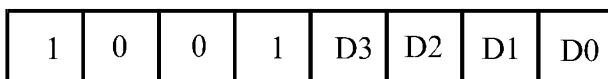
d3	d2	d1	d0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	1	Blink of reverse display
1	0	1	1	Blink of inhibit display

X:invalid

Display mode

CODE	FUNCTION	OPERAND
10010000	Display off	<input type="checkbox"/>
1001xx10	Cursor on, blink off	<input type="checkbox"/>
1001xx11	Cursor on, blink on	<input type="checkbox"/>
100101xx	Text on, graphic off	<input type="checkbox"/>
100110xx	Text off, graphic on	<input type="checkbox"/>
100111xx	Text on, graphic on	<input type="checkbox"/>

X:invalid



(Note)It is necessary to turn on “Text display” and “Graphic display” in the following cases.

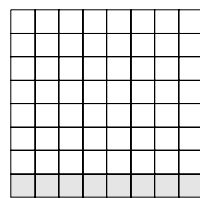
- a)Combination of text/graphic display
- b)Attribute function

Cursor pattern select

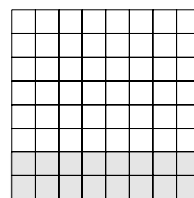
CODE	FUNCTION	OPERAND
10100000	1-line cursor	<input type="checkbox"/>
10100001	2-line cursor	<input type="checkbox"/>
10100010	3-line cursor	<input type="checkbox"/>
10100011	4-line cursor	<input type="checkbox"/>
10100100	5-line cursor	<input type="checkbox"/>
10100101	6-line cursor	<input type="checkbox"/>
10100110	7-line cursor	<input type="checkbox"/>
10100111	8-line cursor	<input type="checkbox"/>

When cursor display is ON, this command selects the cursor pattern in the range 1 line to 8 lines.

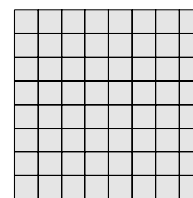
The cursor address is defined by the Cursor Pointer Set command.



1-line cursor



2-line cursor



8-line cursor

Data Auto Read/Write

CODE	HEX.	FUNCTION	OPERAND
10110000	B0H	Set Data Auto Write	<input type="checkbox"/>
10110001	B1H	Set Data Auto Read	<input type="checkbox"/>
10110010	B2H	Auto Reset	<input type="checkbox"/>

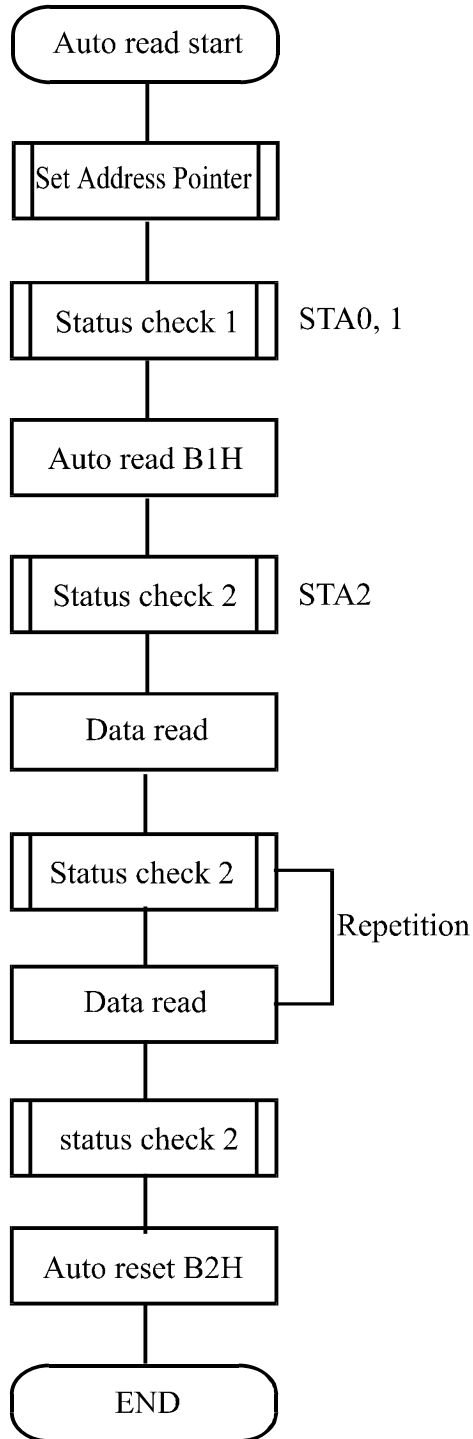
The command is convenient for sending a full screen of data from the external display RAM. After setting Auto mode, a Data Write (or Read) command is need not be sent between each datum. A Data Auto Write (or Read) command must be sent after a Set Address Pointer command. After this command, the address pointer is automatically incremented by 1 after each datum. In Auto mode, the T6963C cannot accept any other commands.

The Auto Reset command must be sent to the T69963C after all data has been sent, to clear Auto mode.

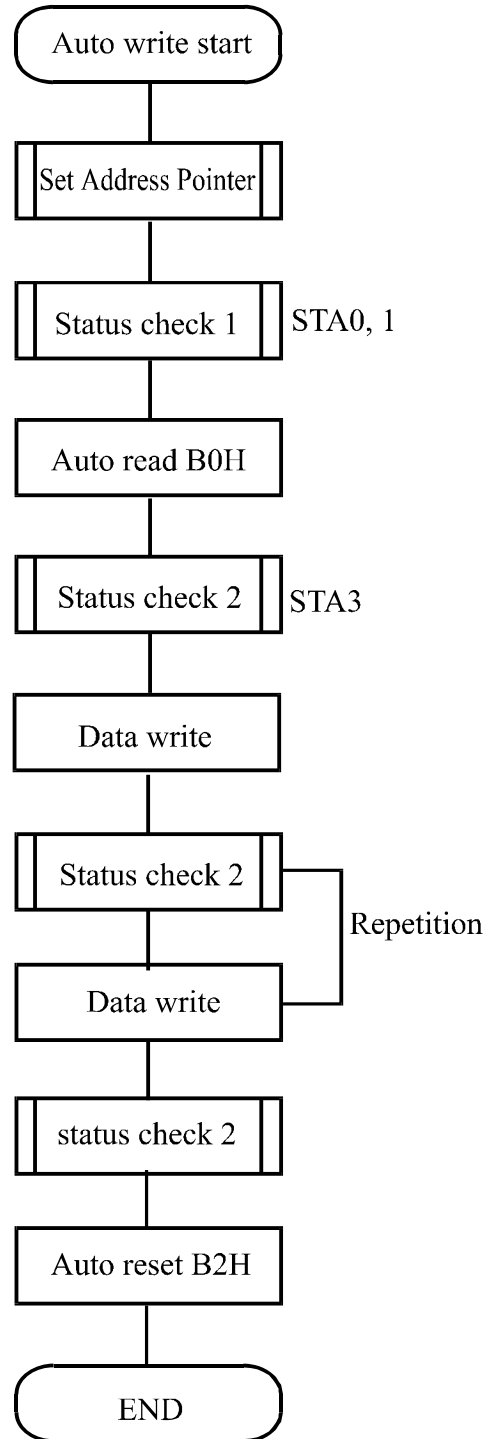
(Note)A Status check for Auto mode

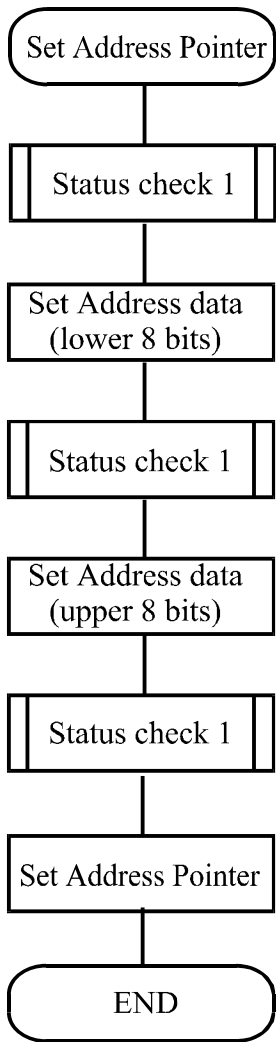
(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1 (STA2=1.) Refer to the following flowchart.

a) Auto Read mode

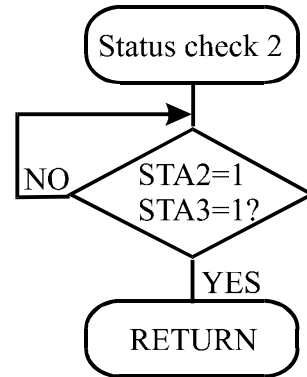
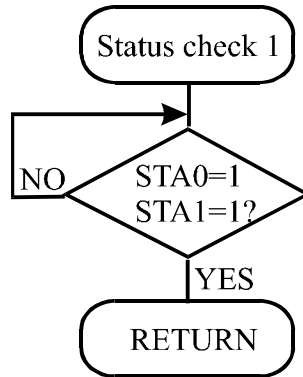


b) Auto Write mode





STA0,1



Send Set Address Pointer command

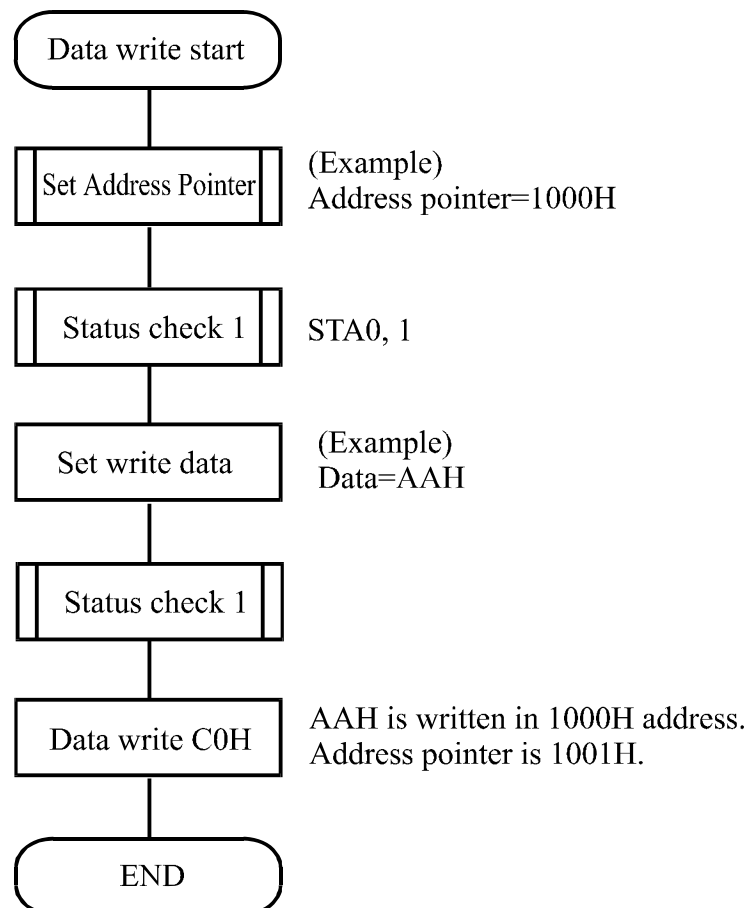
□ Date Read/Write

CODE	HEX.	FUNCTION	OPERAND
11000000	C0H	Data Write and Increment ADP	Data
11000001	C1H	Data Read and Increment ADP	□
11000010	C2H	Data Write and Decrement ADP	Data
11000011	C3H	Data Read and Decrement ADP	□
11000100	C4H	Data Write and Nonvariable ADP	Data
11000101	C5H	Data Read and Nonvariable ADP	□

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Write/Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note) This command is necessary for each 1-byte datum.

Refer to the following flowchart.



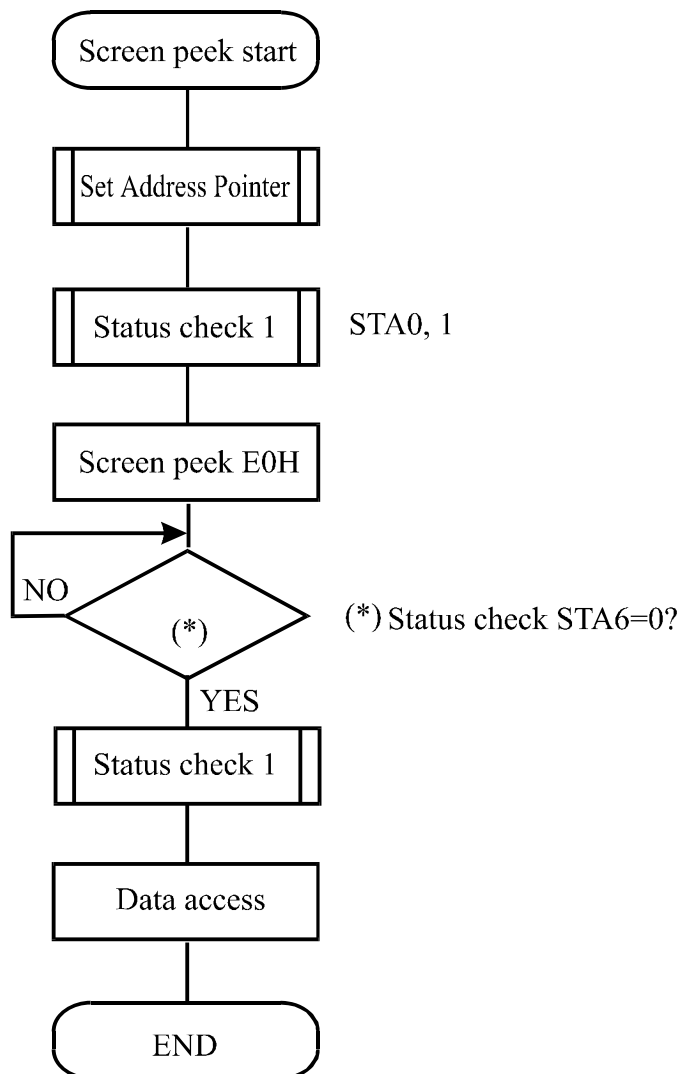
□ Screen Peek

CODE	HEX.	FUNCTION	OPERAND
11100000	E0H	Screen Peek	□e

This command is used to transfer 1 byte of displayed data to the data stack; this byte can then be read from the MPU by data access. The logical combination of text and graphic display data on the LCD screen can be read by this command.

The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this commands is ignored and a status flag (STA6) is set.

Refer to the following flowchart.



□ Screen Copy

CODE	HEX.	FUNCTION	OPERAND
11101000	E8H	Screen Copy	□

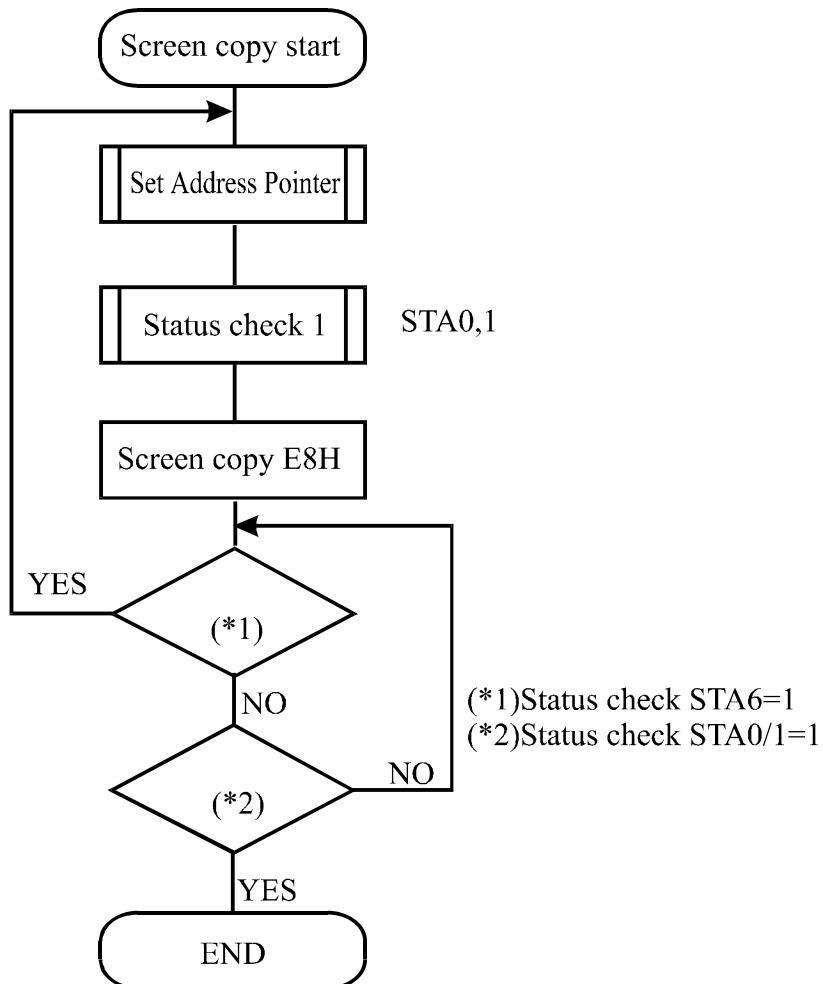
This command copies a single raster line of data to the graphic area.

The start point must be set using the Set Address Pointer command.

(Note 1) If the attribute function is being used, this command is not available.

(With Attribute data is graphic area data.)

Refer to the following flowchart.



Bit Set/Reset

CODE	FUNCTION	OPERAND
11110×××	Bit Reset	<input type="checkbox"/>
11111×××	Bit Set	<input type="checkbox"/>
1111×000	Bit 0 (LSB)	<input type="checkbox"/>
1111×001	Bit 1	<input type="checkbox"/>
1111×010	Bit 2	<input type="checkbox"/>
1111×011	Bit 3	<input type="checkbox"/>
1111×100	Bit 4	<input type="checkbox"/>
1111×101	Bit 5	<input type="checkbox"/>
1111×110	Bit 6	<input type="checkbox"/>
1111×111	Bit 7 (MSB)	<input type="checkbox"/>

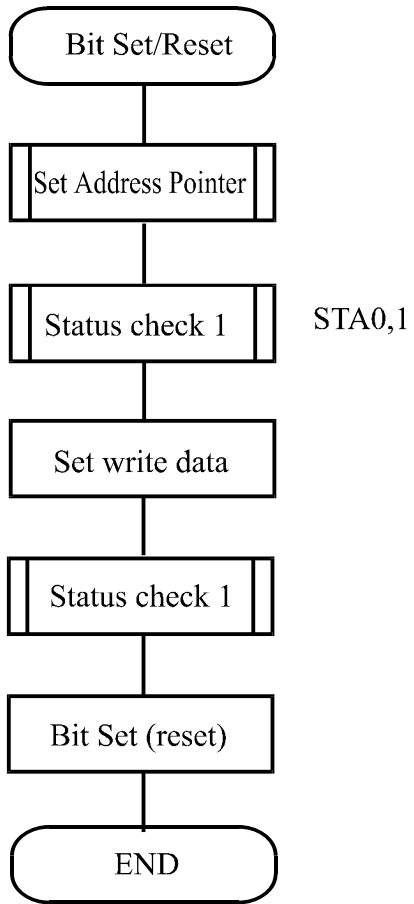
X:invalid

This command use to set or reset a bit of the byte specified by the address pointer.

Only one bit can be set/reset at a time.

Refer to the following flowchart.





## CHARACTERCODEMAP

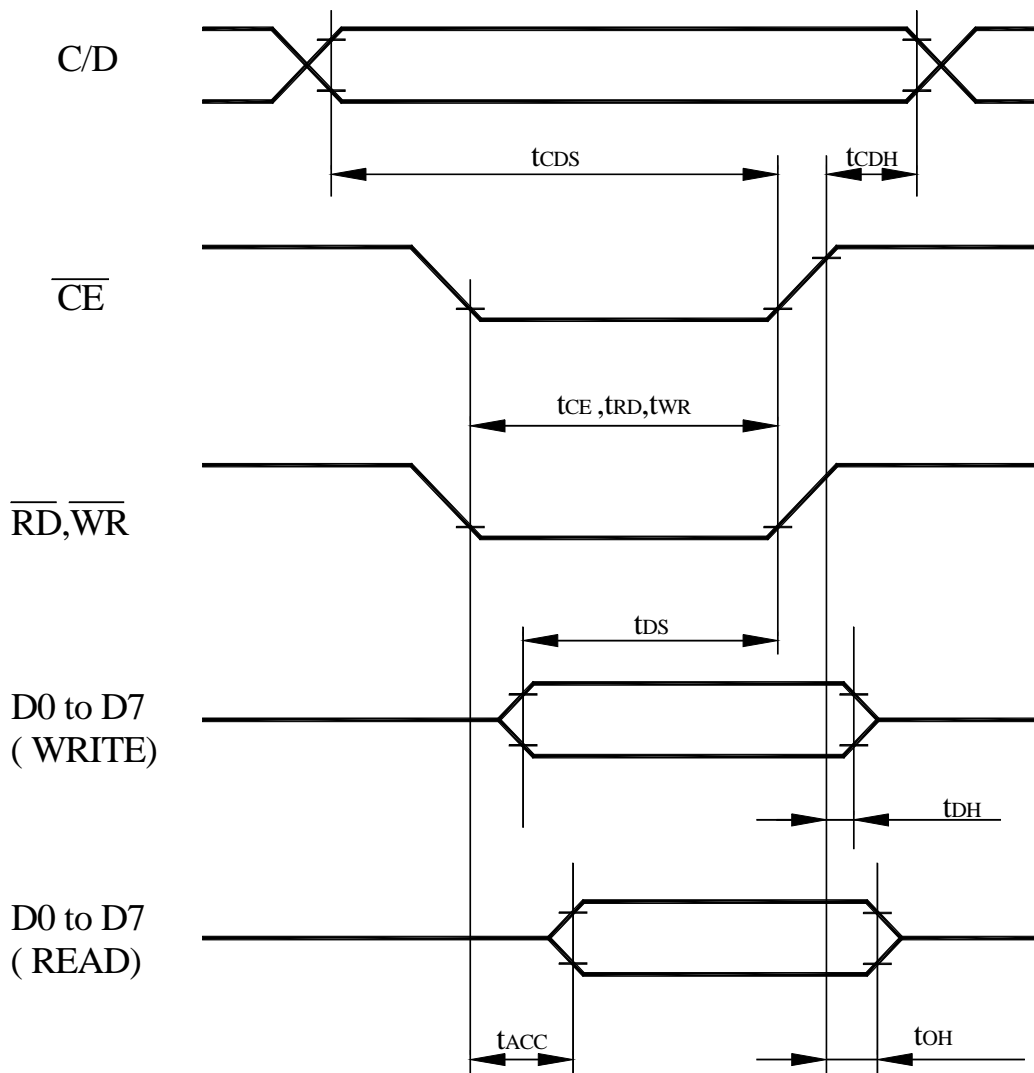
Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH
LLLL		0	1	2	3	4	5	6
LLLH	.	7	8	9	A	B	C	D
LLHL	"	E	F	G	H	I	J	K
LLHH	#	L	M	N	O	P	Q	R
LHLL	*	S	T	U	V	W	X	Y
LHLH	%	Z	[	\	]	^	_	`
LHHL	@	a	b	c	d	e	f	g
LHHH	'	h	i	j	k	l	m	n
HLLL	<	o	p	q	r	s	t	u
HLLH	>	v	w	x	y	z	{	}
HLHL	*	~						
HLHH	+	=						
HHLL	.	<	\					
HHLH	-	=						
HHHL	.	>						
HHHH	/	?						

# 11. Timing Characteristics

## Bus Timing

( $V_{SS} = 0\text{ V}$ ,  $V_{DD} = 5\text{ V}$ )

Item	Symbol	Min	Typ	Max	Unit
C/D Set-up Time	tCDS	100	□	□	ns
C/D Hold Time	tCDH	10	□	□	ns
CE,RD,WR Pulse Width	tCDS,tRD,tWR	80	□	□	ns
Data Set-up Time	tDS	80	□	□	ns
Data Hold Time	tDH	40	□	□	ns
Access Time	tACC	□	□	150	ns
Output Hold Time	tOH	10	□	50	ns



## 12. RELIABILITY

### Content of Reliability Test (wide temperature, -20 ~70 )

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80□ 200hrs	2
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30□ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70□ 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20□ 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60 □,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60□,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation <div style="text-align: center;"> <p style="text-align: center;">-20□    25□    70□  <span style="display: inline-block; border: 1px solid black; border-radius: 50%; padding: 2px 10px;">←</span>  <span style="display: inline-block; border: 1px solid black; border-radius: 50%; padding: 2px 10px;">→</span>            30min    5min    30min            1 cycle</p> </div>	-20□/70□ 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for            Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5k Ω CS=100pF 1 time	—

**Note1: No dew condensation to be observed.**

**Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.**

**Note3: Vibration test will be conducted to the product itself without putting it in a container.**

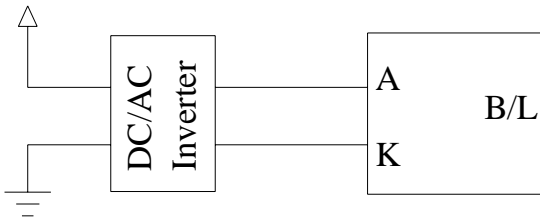
# 13. Backlight Information

## CCFL backlight Specification

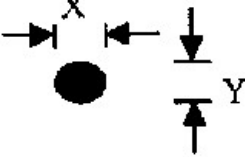
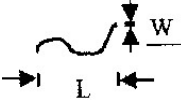
(Ta=25°C)

Item	Symbol	Specification			Unit	Condition
		Min	Typ	Max		
Driving Voltage	V <sub>FL</sub>	□	590	□	Vrms	□
Input current	I <sub>FL</sub>	5.0	5.5	6.0	mArms	□
Luminance	L	□	879	□	Cd/m <sup>2</sup>	φ,θ=0 deg, I <sub>FL</sub> =5.5mArms
Chromaticity	x	□	0.321	□	□	□
	y	□	0.376	□	□	□
Luminance Uniformity (Testing 9 point)	□	75%	□	□	%	φ,θ=0 deg, I <sub>FL</sub> =5.5mArms
Life time	□	17000	□	□	hrs	

CCFL B\L drives directly from A , K.



# 14. Inspection specification

NO	Item	Criterion	AQL																								
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65																								
02	Black or white spots on LCD(display only)	2.1 White and black spots on display $\square 0.25\text{mm}$ , no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5																								
03	LCD black spots, white spots, contamination (non-display)	<p>3.1 Round type : As following drawing</p> <p><math>\Phi = (x + y) / 2</math></p>  <table border="1" data-bbox="852 898 1328 1262"> <thead> <tr> <th>SIZE</th> <th>Acceptable QTY</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \square 0.10</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>0.10 \square \Phi</math></td> <td>2</td> </tr> <tr> <td><math>0.20 \square \Phi</math></td> <td>1</td> </tr> <tr> <td><math>0.25 \square \Phi</math></td> <td>0</td> </tr> </tbody> </table> <p>3.2 Line type : (As following drawing)</p>  <table border="1" data-bbox="690 1304 1328 1625"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable QTY</th> </tr> </thead> <tbody> <tr> <td>---</td> <td><math>W \square 0.02</math></td> <td>Accept no dense</td> </tr> <tr> <td><math>L \square 3.0</math></td> <td><math>0.02 \square W \square 0.03</math></td> <td rowspan="2">2</td> </tr> <tr> <td><math>L \square 2.5</math></td> <td><math>0.03 \square W \square 0.05</math></td> </tr> <tr> <td>---</td> <td><math>0.05 \square W</math></td> <td>As round type</td> </tr> </tbody> </table>	SIZE	Acceptable QTY	$\Phi \square 0.10$	Accept no dense	$0.10 \square \Phi$	2	$0.20 \square \Phi$	1	$0.25 \square \Phi$	0	Length	Width	Acceptable QTY	---	$W \square 0.02$	Accept no dense	$L \square 3.0$	$0.02 \square W \square 0.03$	2	$L \square 2.5$	$0.03 \square W \square 0.05$	---	$0.05 \square W$	As round type	2.5
SIZE	Acceptable QTY																										
$\Phi \square 0.10$	Accept no dense																										
$0.10 \square \Phi$	2																										
$0.20 \square \Phi$	1																										
$0.25 \square \Phi$	0																										
Length	Width	Acceptable QTY																									
---	$W \square 0.02$	Accept no dense																									
$L \square 3.0$	$0.02 \square W \square 0.03$	2																									
$L \square 2.5$	$0.03 \square W \square 0.05$																										
---	$0.05 \square W$	As round type																									

04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.	Size $\Phi$	Acceptable Q TY	2.5
			$\Phi \leq 0.20$	Accept no dense	
			$0.20 < \Phi \leq 0.50$	3	
			$0.50 < \Phi \leq 1.00$	2	
			$1.00 < \Phi$	0	
			Total Q TY	3	

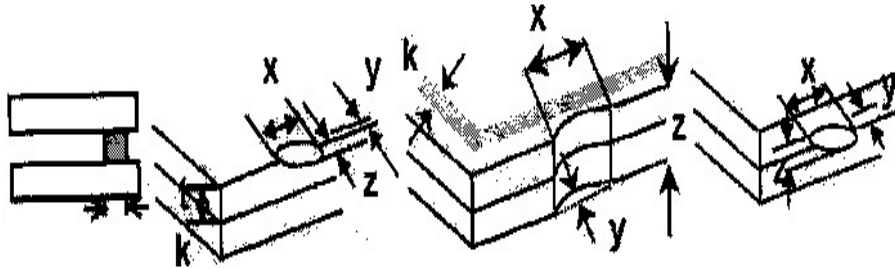
NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination	

Symbols Define:

x: Chip length                      y: Chip width                      z: Chip thickness  
 k: Seal width                      t: Glass thickness                      a: LCD side length  
 L: Electrode pad length:

6.1 General glass chip :

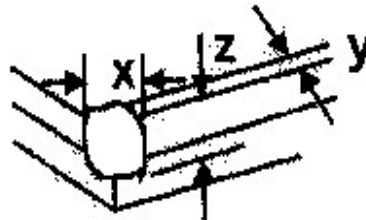
6.1.1 Chip on panel surface and crack between panels:



z: Chip thickness	y: Chip width	x: Chip length
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$
$1/2t \leq z \leq 2t$	Not exceed $1/3k$	$x \leq 1/8a$

□ If there are 2 or more chips, x is total length of each chip.

6.1.2 Corner crack:



z: Chip thickness	y: Chip width	x: Chip length
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$
$1/2t \leq z \leq 2t$	Not exceed $1/3k$	$x \leq 1/8a$

□ If there are 2 or more chips, x is the total length of each chip.

06

Chipped glass

2.5

NO	Item	Criterion	AQL
----	------	-----------	-----

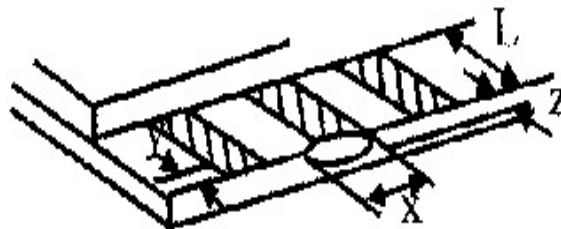


**Symbols :**

x: Chip length                      y: Chip width                      z: Chip thickness  
 k: Seal width                      t: Glass thickness                      a: LCD side length  
 L: Electrode pad length

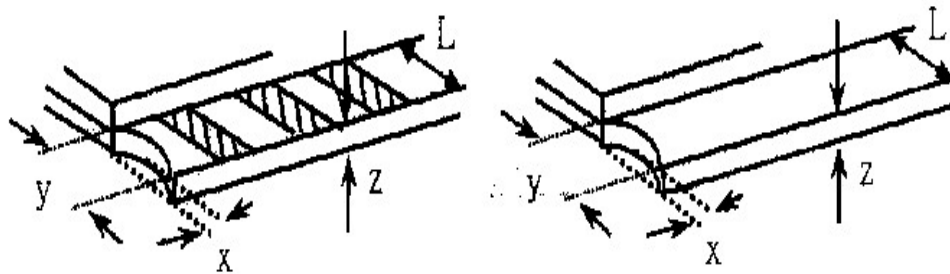
**6.2 Protrusion over terminal :**

**6.2.1 Chip on electrode pad :**



y: Chip width	x: Chip length	z: Chip thickness
$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 \leq z \leq t$

**6.2.2 Non-conductive portion:**

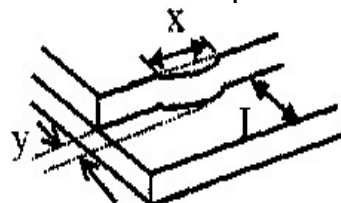


y: Chip width	x: Chip length	z: Chip thickness
$y \leq L$	$x \leq 1/8a$	$0 \leq z \leq t$

If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.

If the product will be heat sealed by the customer, the alignment mark not be damaged.

**6.2.3 Substrate protuberance and internal crack.**



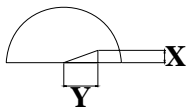
y: width	x: length
$y \leq 1/3L$	$x \leq a$

06

Glass crack

2.5

NO	Item	Criterion	AQL
----	------	-----------	-----

07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB □ COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB  $X * Y \leq 2\text{mm}^2$	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	
		12.11 Product dimension and structure must conform to product specification sheet.	

## 15. Material List of Components for RoHs

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark “#”in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A  The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

2.Process for RoHS requirement

(1) Use the Sn/Ag/Cu soldering surface  the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp.

Reflow  250 , 30 seconds Max.

Connector soldering wave or hand soldering  320 , 10 seconds max.

(3) Temp. curve of reflow, max. Temp.  235±5

Recommended customer’s soldering temp. of connector  280 , 3 seconds.



**1  Panel Specification**

- 1. Panel Type  Pass  NG, \_\_\_\_\_
- 2. View Direction  Pass  NG, \_\_\_\_\_
- 3. Numbers of Dots  Pass  NG, \_\_\_\_\_
- 4. View Area  Pass  NG, \_\_\_\_\_
- 5. Active Area  Pass  NG, \_\_\_\_\_
- 6. Operating Temperature  Pass  NG, \_\_\_\_\_
- 7. Storage Temperature  Pass  NG, \_\_\_\_\_
- 8. Others  \_\_\_\_\_

**2  Mechanical**

- 1. PCB Size  Pass  NG, \_\_\_\_\_
- 2. Frame Size  Pass  NG, \_\_\_\_\_
- 3. Material of Frame  Pass  NG, \_\_\_\_\_
- 4. Connector Position  Pass  NG, \_\_\_\_\_
- 5. Fix Hole Position  A  Pass  NG, \_\_\_\_\_
- 6. Backlight Position  Pass  NG, \_\_\_\_\_
- 7. Thickness of PCB  Pass  NG, \_\_\_\_\_
- 8. Height of Frame to \_\_\_\_\_  Pass  NG, \_\_\_\_\_
- 9. Height of Module  Pass  NG, \_\_\_\_\_
- Others  Pass  NG, \_\_\_\_\_

**3  Relative Hole Size**

- 1. Pitch of Connector  Pass  NG, \_\_\_\_\_
- 2. Hole size of Connector  Pass  NG, \_\_\_\_\_
- 3. Mounting Hole size  Pass  NG, \_\_\_\_\_
- 4. Mounting Hole Type  Pass  NG, \_\_\_\_\_
- 5. Others  Pass  NG, \_\_\_\_\_

**4  Backlight Specification**

- 1. B/L Type  Pass  NG, \_\_\_\_\_
- 2. B/L Color  Pass  NG, \_\_\_\_\_
- 3. B/L Driving Voltage (Reference for LED)  Pass  NG, \_\_\_\_\_
- 4. B/L Driving Current  Pass  NG, \_\_\_\_\_
- 5. Brightness of B/L  Pass  NG, \_\_\_\_\_
- 6. B/L Solder Method  Pass  NG, \_\_\_\_\_
- 7. Others  Pass  NG, \_\_\_\_\_

**Go to page 2**



Module Number  \_\_\_\_\_

Page: 2

**5  Electronic Characteristics of Module**

- |   |                               |                                     |
|---|-------------------------------|-------------------------------------|
| 1. Input Voltage <input type="checkbox"/>           | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Supply Current <input type="checkbox"/>          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Driving Voltage for LCD <input type="checkbox"/> | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Contrast for LCD <input type="checkbox"/>        | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. B/L Driving Method <input type="checkbox"/>      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Negative Voltage Output <input type="checkbox"/> | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Interface Function <input type="checkbox"/>      | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. LCD Uniformity <input type="checkbox"/>          | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. ESD test <input type="checkbox"/>                | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others <input type="checkbox"/>                 | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

**6  Summary**

Sales signature \_\_\_\_\_  
Customer Signature \_\_\_\_\_

Date     /     /     \_\_\_\_\_